To

Application No.	Applicant(s)
10/730,641	FARRAR, PAUL A.
Examiner	Art Unit .
Jesse A. Fenty	2815
	correspondence address application. If not included on will be mailed in due course. THIS t to withdrawal from issue at the initiative
<u>1/058</u> .	
<u>-32 and 34-37</u> .	
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6. ☐ Interview Summa Paper No./Mail D 7. ☑ Examiner's Amen	Date .
	Examiner Jesse A. Fenty Ars on the cover sheet with the OR REMAINS) CLOSED in this are other appropriate communication of the subject and MPEP 1308. 1/058. -32 and 34-37. Examiner. Seen received. Seen received in Application No. uments have been received in the finish application. Ited. Note the attached EXAMINE is reason(s) why the oath or declars are part of this application. Amendment / Comment or in the series of the submitted. Amendment / Comment or in the drawer header according to 37 CFR 1.12 it of BIOLOGICAL MATERIAL OR THE DEPOSIT OF BIOLOGICAL MATERIAL OR THE DEPOSIT OF BIOLOGICAL MATERIAL OR THE DEPOSIT OF BIOLOGICAL Submitted. 5. □ Notice of Information of the drawer header according to 37 CFR 1.12 it of BIOLOGICAL MATERIAL OR THE DEPOSIT OF BIOLOGICAL MATERIAL OR THE DEPOSIT OF BIOLOGICAL Submitted. 5. □ Notice of Information of the drawer header according to 37 CFR 1.12 it of BIOLOGICAL MATERIAL OR THE DEPOSIT OF BIOLOGICAL MATERIAL OR THE DEPOSIT OF BIOLOGICAL Submitted Sub

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EXAMINER'S AMENDMENT

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1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the Title, before "Low", insert -- A Semiconductor Device Comprising --.

1. The following is an examiner's statement of reasons for allowance: Applicant's arguments regarding the deficiencies in the prior art are persuasive to demonstrate that the semiconductor device comprising at least a dielectric layer including an air gap for location at least partially beneath an active region, a semiconductor layer formed over the dielectric layer, a trench formed in the substrate between two active regions, wherein the trench contains cells of gaseous components and extends at least partially into a level of the dielectric layer of the substrate is neither anticipated nor obvious over the prior art of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jesse A. Fenty whose telephone number is 571-272-1729. The examiner can normally be reached on 5/4-9 1st Fri. Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 571-272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jesse A. Fenty Examiner Art Unit 2815

> JEROME JACKSON PRIMARY EXAMINER